

FIG.1

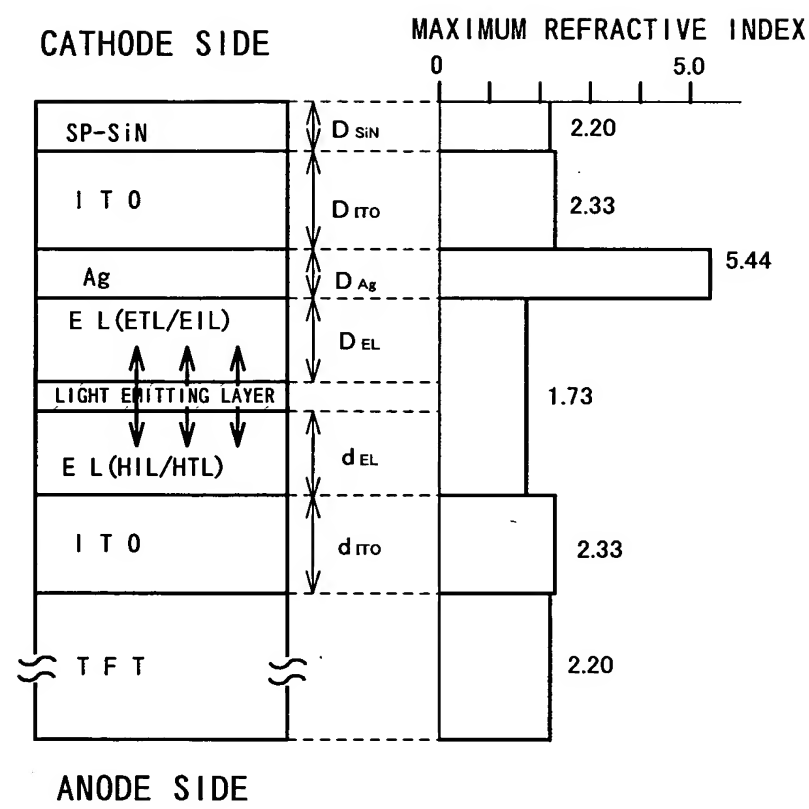


FIG.2

ANODE SIDE	THICKNESS(nm)	CATHODE SIDE	THICKNESS(nm)
0 LAYER(LIGHT EMITTING LAYER)	0	0 LAYER(LIGHT EMITTING LAYER)	0
1st(HTL/HIL)	33	1st LAYER(ETL/EIL)	90
2nd(ITO)	110	2nd LAYER(Ag)	8
※3rd(SP-SiN)	100	3rd LAYER(ITO)	260
※4th(ACRYLIC RESIN)	1000	4th LAYER(SP-SiN)	100
※5th(SiNO)	100	5th LAYER(AIR)	0
※6th(GI)	110	GLASS SUBSTRATE	
※7th(SiON)	100		
※8th(SiNO)	50		
GLASS SUBSTRATE			



LAMINATED STRUCTURE

ANODE LAYER TFT\ITO\EL(HIL\HTL\EML)\EL(EML\ETL\EIL)\Ag\ITO\SP-SiN CATHODE LAYER

↑
LIGHT
EMITTING
LAYER

FIG.3A

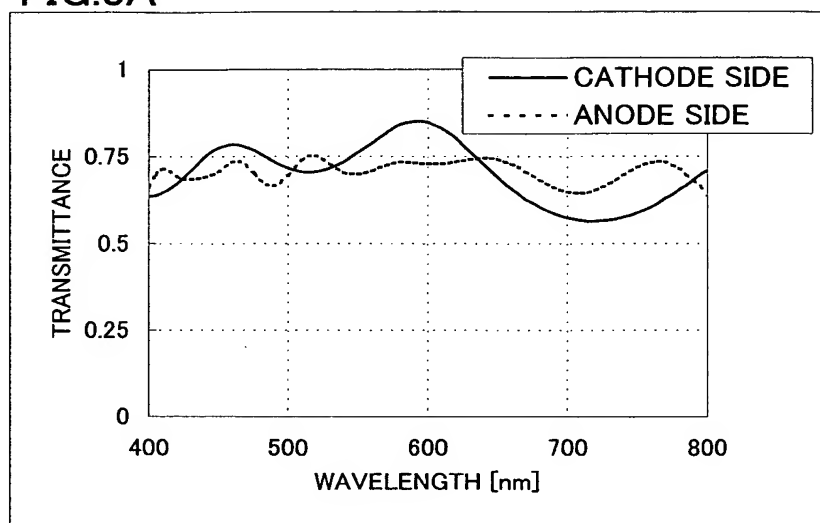


FIG.3B

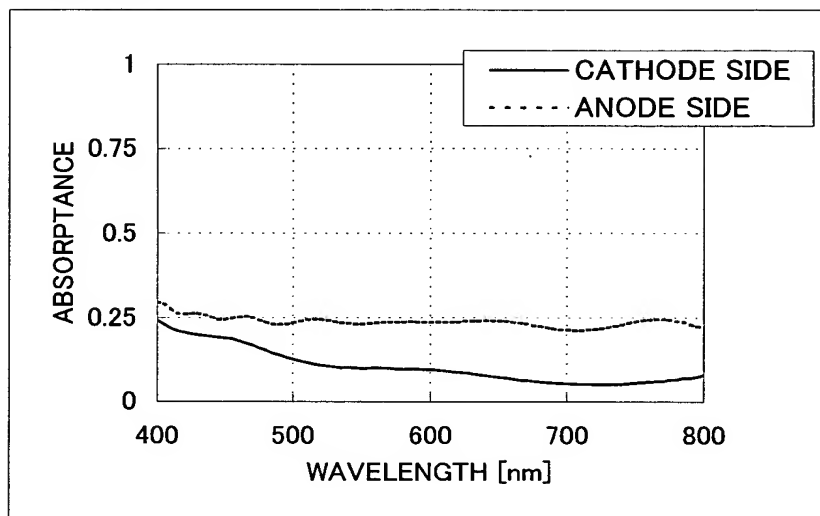


FIG.3C

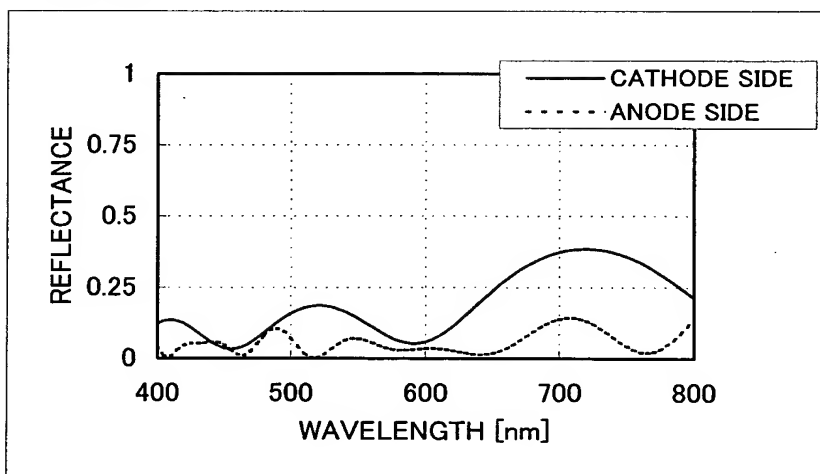


FIG. 4A

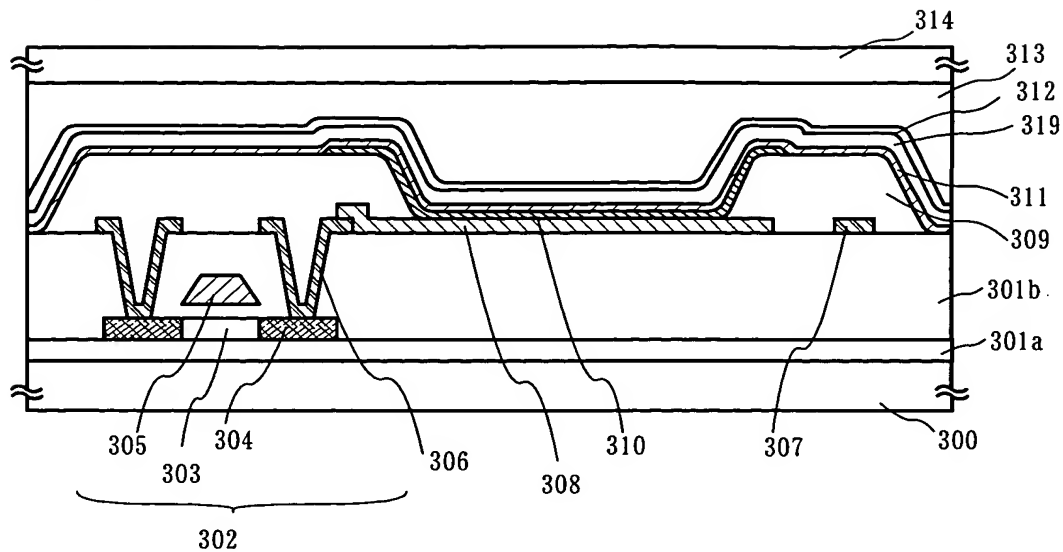


FIG. 4B

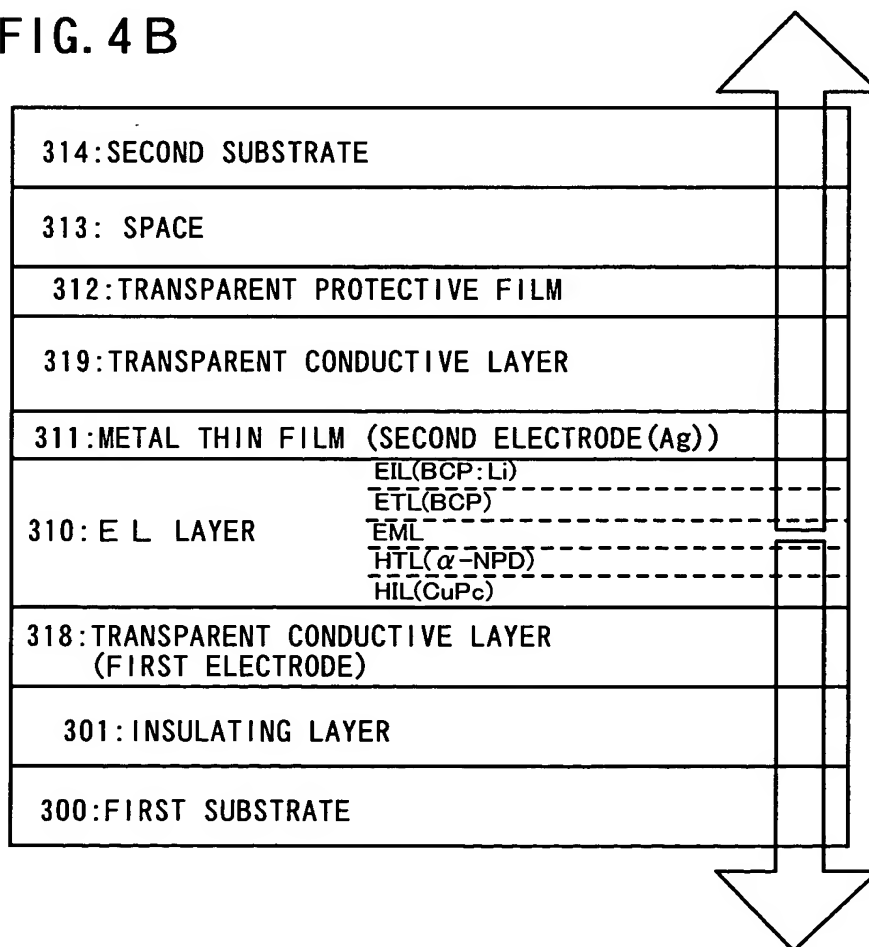


FIG. 5A

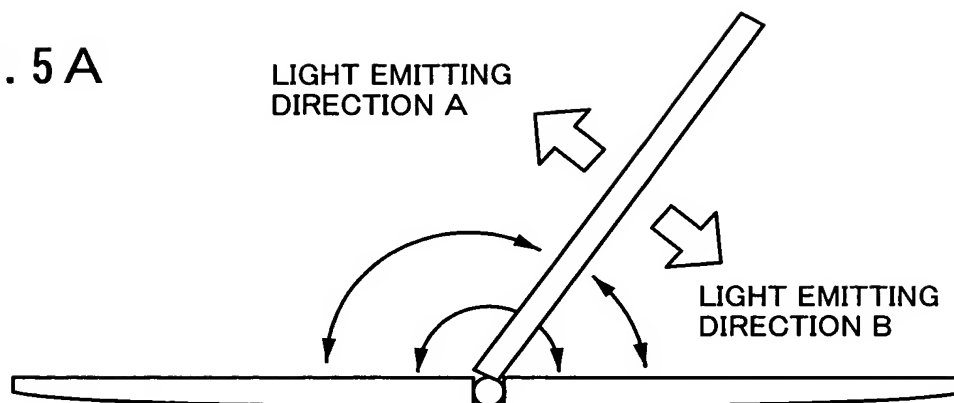


FIG. 5B

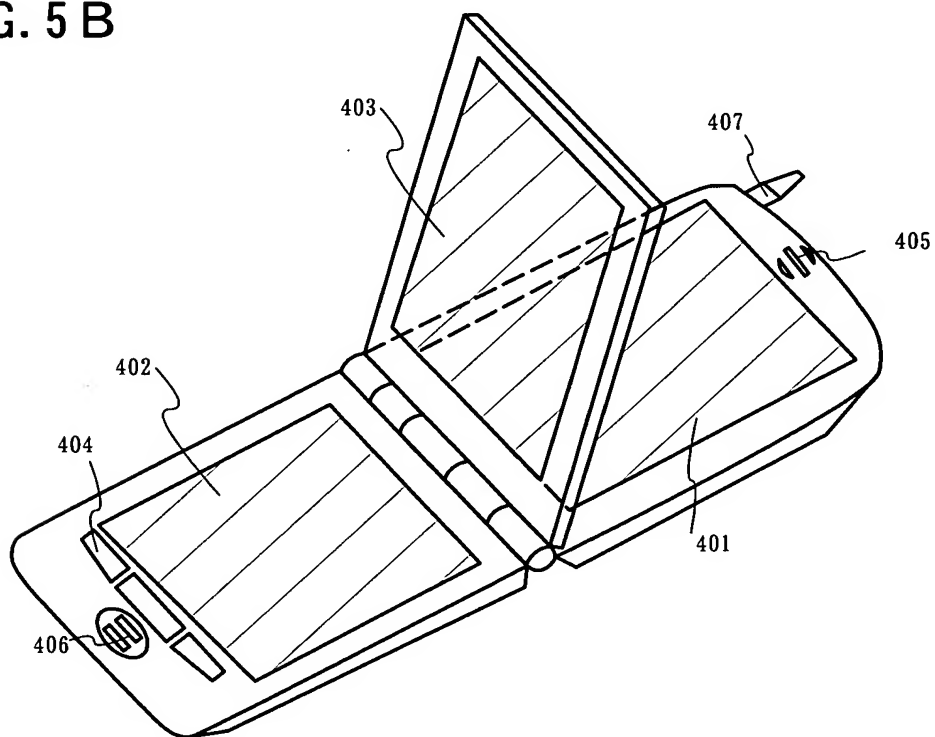


FIG. 6A

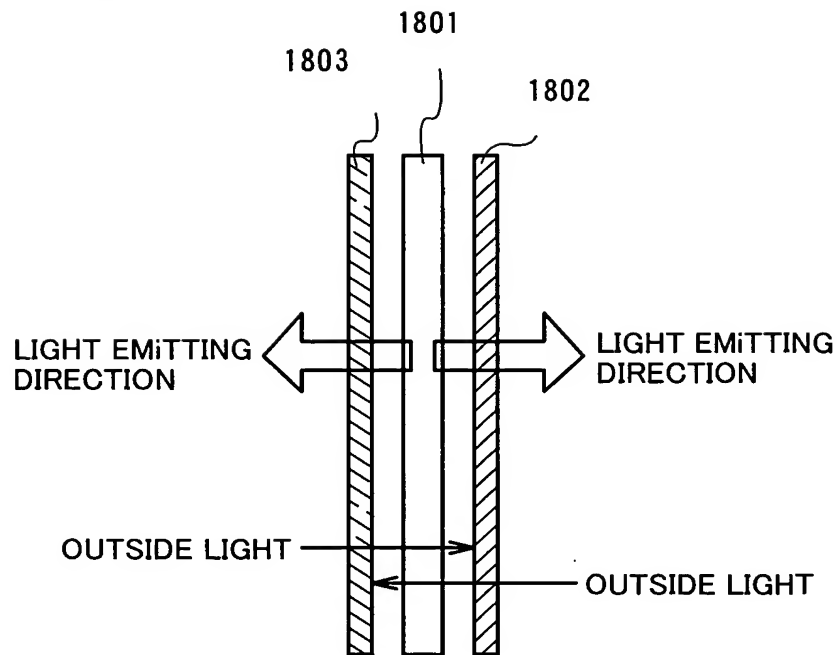


FIG. 6B

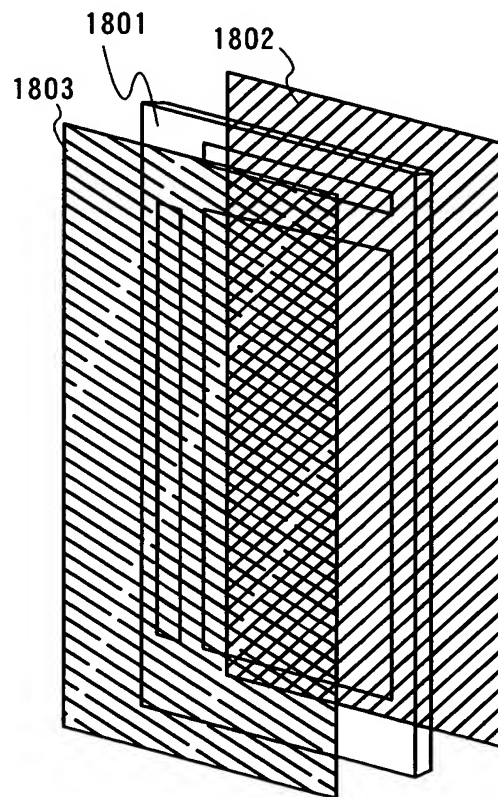


FIG.7

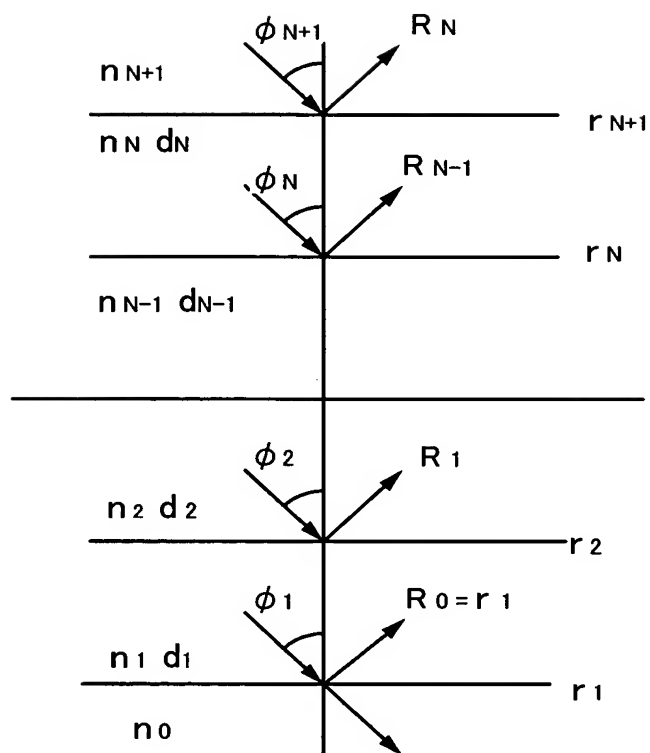


FIG.8

